The Relative Power of Synchronization Operations Queues and Stacks



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Based on slides by Maurice Herlihy and Nir Shavit

Why is Mutual Exclusion so wrong?

Asynchronous Interrupts



Heterogeneous Processors



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Fault-tolerance



Wait-Free Implementations

Definition: An object implementation is wait-free if every thread completes a method in a finite number of steps

No mutual exclusion

- Thread could halt in critical section
- Build mutual exclusion from registers

Lock-Free Implementations

Definition: An object implementation is lock-free if in an infinite execution infinitely often some method call finishes (obviously, in a finite number of steps)

No difference between lock-free and wait-free for finite executions

Basic Questions

- Wait-Free synchronization might be a good idea in principle
- But how do you do it
 - Systematically?
 - Correctly?
 - Efficiently?

Today: Focus on Wait-free

- The rest of today's discussion will focus on wait-free implementations
- But the results we present apply almost verbatim to lock-free ones

FIFO Queue: Enqueue Method



FIFO Queue: Dequeue Method



Two-Thread Wait-Free Queue

```
public class LockFreeQueue {
 int head = 0, tail = 0;
 Item[QSIZE] items;
 public void eng(Item x) throws... {
  if (tail-head == QSIZE) { throw...};
  items[tail % QSIZE] = x; tail++;
  }
 public Item deq() {
  if (tail-head == 0) { throw...}
  Item item = items[head % QSIZE];
  head++; return item;
} }
```

Two-Thread Wait-Free Queue

```
public class LockFreeQueue {
 int head = 0, tail = 0;
 Item[QSIZE] items;
 public void enq(Item x) throws... {
  if (tail-head == QSIZE) { throw...};
 items[tail % QSIZE] = x; tail++;
 public Item deq() {
  if (tail-head == 0) Putebject in queue
  Item item = items[head % QSIZE];
  head++; return item;
} }
```

Two-Thread Wait-Free Queue

```
public class LockFreeQueue {
 int head = 0, tail = 0;
 Item[QSIZE] items;
 public void enq(Item x) throws... {
  if (tail-head == QSIZE) { throw...};
  items[tail % QSIZE] = x; tail++;
public Item deq() { Increment tail
  if (tail-head == 0) { thrownter
  Item item = items[head % QSIZE];
  head++; return item;
} }
```

What About Multiple Dequeuers?





- Wait-free
- Linearizable
- From atomic read-write registers
- Multiple dequeuers

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Consensus

- While you are thinking about the grand challenge...
- We will give you another puzzle
 - Consensus
 - Will be important ...



They Communicate





Formally: Consensus

- Consistent: all threads decide the same value
- Valid: the common decision value is some thread's input
- Wait-free: each thread decides after a finite number of steps



Formally

- Theorem [adapted from Fischer, Lynch, Paterson]: There is no wait-free implementation of n-thread consensus, n>1, from read-write registers even if only one thread can crash
- Implication: asynchronous computability fundamentally different from Turing computability

Proof Strategy

- Assume otherwise
- Reason about the properties of any such protocol
- Derive a contradiction
- Quod Erat Demonstrandum
- Suffices to prove for binary consensus and n=2

Wait-Free Computation



- Either A or B "moves"
- Moving means
 - Register read
 - Register write





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Univalent: Single Value Possible



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Claim

- Some initial state is bivalent
- Outcome depends on
 - Chance
 - Behavior of the scheduler
- Lets prove this claim



Univalent: all executions must decide 0



Including this solo execution by A



All executions must decide 1



Including this solo execution by B

What if inputs differ?



By Way of contradiction: If univalent all executions must decide on same value
The Possible Executions



Include the solo execution by A that decides 0

The Possible Executions



Also include the solo execution by B which we know decides 1



 Solo execution by A must decide 0 Solo execution by B must decide 1

Summary So Far

- Wait-free computation is a tree
- Bivalent system states
 - Outcome not fixed
- Univalent states
 - Outcome is fixed
 - May not be "known" yet
- 1-Valent and 0-Valent states





Reaching Critical State



Critical States

- Starting from a bivalent initial state
- The protocol can reach a critical state
 - Otherwise we could stay bivalent forever
 - And the protocol is not wait-free

Model Dependency

- So far, memory-independent!
- True for
 - Registers
 - Message-passing
 - Carrier pigeons
 - Any kind of asynchronous computation

What are the Threads Doing?

- Reads and/or writes
- To same/different registers

Completing the Proof

- Lets look at executions that:
 - Start from a critical state
 - Threads cause state to become univalent by reading or writing to same/different registers
 - End within a finite number of steps deciding either 0 or 1
- Show this leads to a contradiction

Possible Interactions					
			A reads x		
	x.read()	y.read()	x.write()	y.write()	
x.read()	?	?	?	?	
y.read()	?	?	?	?	
x.write()	?	?	?	?	
y.write()	?	?	?	?	
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Some Thread Reads



Possible Interactions

	x.read()	y.read()	x.write()	y.write()
x.read()	no	no	no	no
y.read()	no	no	no	no
x.write()	no	no	?	?
y.write()	no	no	?	?
		$^{\mid}_{\odot}$ Herlihy and Shav	l it	50



Possible Interactions

	x.read()	y.read()	x.write()	y.write()
x.read()	no	no	no	no
y.read()	no	no	no	no
x.write()	no	no	?	no
y.write()	no	no	no	?
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That's All, Folks!

	x.read()	y.read()	x.write()	y.write()	
x.read()	no	no	no	no	
y.read()	no	no	no	no	
x.write()	no	no	no		
y.write()	no	no	no	119	
	© Herlihy and Shavit 54				

Recap: Atomic Registers Can't Do Consensus

- If protocol exists
 - It has a bivalent initial state
 - Leading to a critical state
- What's up with the critical state?
 - Case analysis for each pair of methods
 - As we showed, all lead to a contradiction

What Does Consensus have to do with Concurrent Objects?



Consensus Object

public interface Consensus {
 Object decide(Object value);
}

Concurrent Consensus Object

- We consider only one time objects: each thread can execute a method only once
- Linearizable to sequential consensus object in which
 - the thread who's input was decided on completed its method first

Java Jargon Watch

- Define Consensus protocol as an abstract class
- We implement some methods
- Leave you to do the rest ...

abstract class ConsensusProtocol
implements Consensus {
 protected Object[] proposed =
 new Object[N];

```
private void propose(Object value) {
  proposed[ThreadID.get()] = value;
}
```

```
abstract public Object
    decide(Object value);
```

}}



abstract class ConsensusProtocol
implements Consensus {
 protected Object[]
 new Object[N];
 Propose a value

private void propose(Object value) {
 proposed[ThreadID.get()] = value;

abstract public Object decide(object value);

} }

Decide a value: abstract method means subclass does the heavy lifting (real work)



abstract public Object
 decide(object value);

Can FIFO Queue Implement Consensus?

FIFO Consensus



propose array



FIFO Queue with red and black balls

Protocol: Write Value to Array





Protocol: Take Next Item from Queue I got the dreaded I got the coveted black ball, so I will red ball, so I will decide the other's decide my value value from the array 8

Consensus Using FIFO Queue

```
public class QueueConsensus
  extends ConsensusProtocol {
private Queue queue;
public QueueConsensus() {
  queue = new Queue();
  queue.eng(Ball.RED);
  queue.enq(Ball.BLACK);
 }
```

Initialize Queue



Who Won?

```
public class QueueConsensus
  extends ConsensusProtocol {
private Queue queue;
 ...
public decide(object value) {
  propose(value);
  Ball ball = this.queue.deq();
  if (ball == Ball.RED)
   return proposed[i];
  else
   return proposed[1-i];
```

Who Won?


Who Won?

```
public class QueueConsensus
  extends ConsensusProtocol {
 private Queue queue;
 ...
 public decide(object value) {
  propose(value);
           = this.queue.deq();
  if (ball == Ball.RED)
   return proposed[i];
  else
                          i = ThreadID.get();
   return proposed[1-i
                          I win if I was first
```

Who Won?

public class QueueConsensus
 extends ConsensusProtocol {
 private Queue queue;

Other thread wins if I
public decide(object value) was second
propose(value);
Ball ball = this.guene.deq();
if (ball == Ball.REI)
return proposed[i];
else
return proposed[1-i];
}

Why does this Work?

- If one thread gets the red ball
- Then the other gets the black ball
- Winner decides her own value
- Loser can find winner's value in array
 - Because threads write to array
 - Before dequeueing from queue

Theorem

- We can solve 2-thread consensus using only
 - A two-dequeuer queue, and
 - Some atomic registers

Implications

- Given
 - A consensus protocol from queue and registers
- Assume there exists
 - A queue implementation from atomic registers
- Substitution yields:
 - A wait-free consensus protocol from atomic On registers

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Corollary

- It is impossible to implement
 - a two-dequeuer wait-free FIFO queue
 - from read/write memory.

Consensus Numbers

- An object X has consensus number n
 - If it can be used to solve n-thread consensus
 - Taking any number of instances of X
 - together with atomic read/write registers
 - and implement n-thread consensus
 - But not (n+1)-thread consensus

Consensus Numbers

• Theorem

- Atomic read/write registers have consensus number 1
- Theorem
 - Multi-dequeuer FIFO queues have consensus number at least 2

Consensus Numbers Measure Synchronization Power

- Theorem
 - If you can implement X from Y
 - And X has consensus number c
 - Then Y has consensus number at least c

Synchronization Speed Limit

- Conversely
 - If X has consensus num
 - And Y has consensus n
 - Then there is no way to confree implementation of X by
- This theorem will be very useful
 - Unforeseen practical implications!

eoretical

at: Certain

Homework

- What is the consensus number of a wait-free FIFO queue with methods:
 - enq(o): enqueue object o
 - deq(): dequeue first object
 - peek(): get a copy of first object

New Grand Challenge

• Consider:

- Write multiple array elements atomically
- Scan any array elements
- Call this problem multiple assignment

Multiple Assignment Theorem

- Atomic registers cannot implement multiple assignment
- Weird or what?
 - Single location write/multiple location read OK (= Atomic Snapshot)
 - Multi location write/single location read impossible

Atomic Snapshot



Atomic Snapshot

- Array of MRSW atomic registers
- Take instantaneous snapshot of all
- Generalizes to MRMW registers ...

Snapshot Interface

public interface Snapshot {
 public int update(int v);
 public int[] scan();

}



Snapshot Interface

Instantaneous snapshot of all theads' registers

public interface Snapshot {
 public int update(int v);
 public int[] scan();

Atomic Snapshot

Collect

- Read values one at a time
- Problem
 - Incompatible concurrent collects
 - Result not linearizable

Example: Atomic Snapshot MRMW



 \bigcirc read \swarrow update A: 1, 3, 3 B: 2, 2, 2

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Clean Collects

- Clean Collect
 - Collect during which nothing changed
 - Can we make it happen?
 - Can we detect it?

Simple Snapshot

- Put increasing labels on each entry
- Collect twice
- If both agree,
 - We're done
- Otherwise,
 - Try again



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Simple Snapshot: Update

public class SimpleSnapshot implements Snapshot {
 private AtomicMRSWRegister[] register;

```
public void update(int value) {
    int i = Thread.myIndex();
        LabeledValue oldValue = register[i].read();
    LabeledValue newValue =
        new LabeledValue(oldValue.label+1, value);
    register[i].write(newValue);
```

}

Simple Snapshot: Update



One single-writer register per thread

Simple Snapshot: Update

public class SimpleSnapshot implements Snapshot {
 private AtomicMRSWRegister[] register;

public void update(int value) {

int i = Thread.myIndex();

LabeledValue oldValue = register[i].read();

LabeledValue newValue =

new LabeledValue(oldValue.label+1, value);

register[i].write(newValue);

Write each time with higher label

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Simple Snapshot: Collect

private LabeledValue[] collect() { LabeledValue[] copy = new LabeledValue[n]; for (int j = 0; j < n; j++) copy[j] = this.register[j].read(); return copy;

}

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Simple Snapshot



Just read each register into array

```
public int[] scan() {
  LabeledValue[] oldCopy, newCopy;
  oldCopy = collect();
  collect: while (true) {
    newCopy = collect();
    if (!equals(oldCopy, newCopy)) {
      oldCopy = newCopy;
      continue collect;
     }
    return getValues (newCopy);
```



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Example



 $\bigcirc read \\ \swarrow update$

B: 2, 2, 2 B: 2, 2, 2

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Simple Snapshot

- Linearizable
- Update is wait-free
 - No unbounded loops
- But Scan can starve
 - If interrupted by concurrent update

Wait-Free Snapshot

- Add a scan before every update
- Write resulting snapshot together with update value
- If scan is continuously interrupted by updates, scan can take the update's snapshot

Wait-free Snapshot

If A's scan observes that B moved <u>twice</u>, then B completed an update while A's scan was in progress




Α



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Α





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B's 1st update must have written during 1st collect





Once is not Enough



Someone Must Move Twice



If we collect n times...some thread Must move twice (Pigeon hole)

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Scan is Wait-free



public class SnapValue {
 public int label;
 public int value;
 public int[] snap;
}







Wait-free Update

public void update(int value) { int i = Thread.myIndex(); int[] snap = this.scan(); SnapValue oldValue = r[i].read(); SnapValue newValue = new SnapValue(oldValue.label+1, value, snap); r[i].write(newValue); }





```
public int[] scan() {
  SnapValue[] oldCopy, newCopy;
 boolean[] moved = new boolean[n];
 oldCopy = collect();
  collect: while (true) {
 newCopy = collect();
  for (int j = 0; j < n; j++) {
   if (oldCopy[j].label != newCopy[j].label) {
      ...
  }}
  return getValues(newCopy);
}}
```





```
public int[] scan() {
  SnapValue[] oldCopy, newCopy;
  boolean[] moved = new boolean[n];
  oldCopy = collect();
  collect: while (true) {
  newCopy = collect();
   or (int j = 0; j < n; j++) {
    if (oldCopy[j].label != newCopy[j].label) {
      ...
  } }
  return getValues(newCopy);
If mismatch detected...lets
} } }
                         expand here...
     (2)
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```

Mismatch Detected

```
if (oldCopy[j].label != newCopy[j].label) {
   if (moved[j]) { // second move
    return newCopy[j].snap;
   } else {
    moved[j] = true;
    oldCopy = newCopy;
    continue collect;
  }}
  return getValues(newCopy);
}}
```

Mismatch Detected



Mismatch Detected



Snapshot Summary

• We saw that we can build wait-free atomic snapshot from atomic registers

Multiple Assignment Theorem

- Atomic registers cannot implement multiple assignment
- Weird or what?
 - Single location write/multi location read
 OK
 - (= Atomic Snapshot)
 - Multi location write/single location read impossible

Proof Strategy

- If we can write to 2/3 array elements
 - We can solve 2-consensus
 - Impossible with atomic registers
- Therefore
 - Cannot implement multiple assignment with atomic registers

Proof Strategy

- Take a 3-element array
 - A writes atomically to slots 0 and 1
 - B writes atomically to slots 1 and 2
 - Any thread can scan any set of locations

Double Assignment Interface

Double Assignment Interface



Double Assignment Interface





Thread A wins if

Thread B didn't move

Thread A wins if

Thread B moved later

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Thread A loses if

Thread B moved earlier

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```
class MultiConsensus extends Consensus{
Assign2 a = new Assign2(3, EMPTY);
public Object decide(object value) {
  a.assign(i, i, i+1, i);
  int other = a.read((i+2) \otimes 3);
  if (other==EMPTY||other==a.read(1))
   return proposed[i];
  else
   return proposed[j];
  }
```

- class MultiConsensus extends Consensus{ Assign2 a = new Assign2(3, EMPTY);public Object decide(object value) { a.assign(i, i, i+1, i); int other = a.read((i+2) % 3); if (other==EMPTY||other==a.read(1)) return proposed[i]; else return proposed[j]; Extends ConsensusProtocol
 - Decide sets j=1-i and proposes value



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class MultiConsensus extends Consensus{ Assign2 a = new Assign2(3, EMPTY); public Object decide(object value) { a.assign(i, i, i+1, i); int other = a.read((i+2) % 3);if (other==EMPTY other==a.read(1)) return proposed[i] else return proposed[j]; Assign id 0 to entries } } 0,1 (or id 1 to entries 1,2)










Summary

- If a thread can assign atomically to 2 out of 3 array locations
- Then we can solve 2-consensus
- Therefore
 - No wait-free multi-assignment from read/write registers

Read-Modify-Write Objects

- Method call
 - Returns object's prior value x
 - Replaces x with mumble(x)

```
public abstract class RMWRegister {
  private int value;
```

```
public void synchronized
getAndMumble() {
    int prior = this.value;
    this.value = mumble(this.value);
    return prior;
}
```





RMW Everywhere!

- Most synchronization instructions
 - are RMW methods
- The rest
 - Can be trivially transformed into RMW methods

Example: Read

```
public abstract class RMWRegister {
    private int value;
```

```
public int synchronized read() {
    int prior = this.value;
    this.value = this.value;
    return prior;
}
```

Example: Read



Example: getAndSet

```
public abstract class RMWRegister {
  private int value;
```

```
public void synchronized
  getAndSet(int v) {
  int prior = this.value;
  this.value = v;
  return prior;
}
```

Example: getAndSet (swap)



getAndIncrement

```
public abstract class RMWRegister {
  private int value;

  public void synchronized
    getAndIncrement() {
    int prior = this.value;
    this.value = this.value + 1;
    return prior;
```

}

getAndIncrement



getAndAdd

```
public abstract class RMWRegister {
  private int value;
```

```
public void synchronized
  getAndAdd(int a) {
  int prior = this.value;
  this.value = this.value + a;
  return prior;
}
```

Example: getAndAdd



```
public abstract class RMWRegister {
 private int value;
 public boolean synchronized
   compareAndSet(int expected,
                  int update) {
  int prior = this.value;
  if (this.value==expected) {
   this.value = update; return true;
  }
  return false;
  } ... }
```





```
public abstract class RMWRegister {
 private int value;
 public boolean synchronized
   compareAndSet(int expected,
                  int update) {
 int prior = this.value;
 if (this.value==expected) {
 this.value = update; return true;
  }
 return false;
                       Report' success
 } ... }
```

```
public abstract class RMWRegister {
 private int value;
 public boolean synchronized
   compareAndSet(int expected,
                 int update) {
 int prior = this.value;
 if (this.value==expected) {
  this.value = update; return true;
                       Otherwise report
return false;
                            failure
   ...
```

public abstract class RMWRegister {
 private int value;

public void synchronized
getAndMumble() {
 int prior = this.value;
 this.value = mumble(this.value);
 return prior;
}

Let's characterize F(x)...

Definition

- A RMW method
 - With function mumble(x)
 - is non-trivial if there exists a value v
 - Such that $v \neq mumble(v)$

Par Example

- Identity(x) = x
 - is trivial
- getAndIncrement(x) = x+1
 - is non-trivial

Theorem

- Any non-trivial RMW object has consensus number at least 2
- No wait-free implementation of RMW registers from atomic registers
- Hardware RMW instructions not just a convenience

Reminder

- Subclasses of consensus have
 - -propose(x) method
 - which just stores x into proposed[i]
 - Built-in method
 - -decide(object value) method
 - which determines winning value
 - Customized, class-specific method

```
public class RMWConsensus
     implements ConsensusProtocol {
 private RMWRegister r = v;
 public Object decide(object value) {
  propose(value);
  if (r.getAndMumble() == v)
   return proposed[i];
  else
   return proposed[j];
} }
```





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- We have displayed
 - A two-thread consensus protocol
 - Using any non-trivial RMW object

Interfering RMW

- Let F be a set of functions such that for all f_i and f_j either
 - Commute: $f_i(f_j(v))=f_j(f_i(v))$
 - Overwrite: $f_i(f_j(v))=f_i(v)$
- Claim: Any set of RMW objects that commutes or overwrites has consensus number exactly 2
Examples

 "test-and-set" getAndSet(1) f(v)=1 **Overwrite** $f_i(f_i(v)) = f_i(v)$ • "swap" getAndSet(x) f(v)=x **Overwrite** $f_i(f_i(v)) = f_i(v)$ "fetch-and-inc" getAndIncrement() f(v)=v+1 Commute $f_i(f_i(v)) = f_i(f_i(v))$

Meanwhile Back at the Critical State



Maybe the Functions Commute



Maybe the Functions Commute



Maybe the Functions Overwrite





Impact

- Many early machines provided these "weak" RMW instructions
 - Test-and-set (IBM 360)
 - Fetch-and-add (NYU Ultracomputer)
 - Swap (Original SPARCs)
- We now understand their limitations
 - But why do we want consensus anyway?

compareAndSet

```
public abstract class RMWRegister {
private int value;
public boolean synchronized
  compareAndSet(int expected,
                 int update) {
 int prior = this.value;
 if (this.value==expected) {
  this.value = update; return true;
 }
 return false;
 } ... }
```

compareAndSet



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```
public class RMWConsensus
    implements ConsensusProtocol {
private AtomicInteger r =
  new AtomicInteger(-1);
public Object decide(object value) {
 propose(value);
 r.compareAndSet(-1,i);
 return proposed[r.get()];
```



(4)



(4)



(4)

The Consensus Hierarchy

1 Read/Write Registers, Snapshots...

2 getAndSet, getAndIncrement, ...

∞ compareAndSet,...

Multiple Assignment

- Atomic k-assignment
- Solves consensus for 2k-2 threads
- Every even consensus number has an object (can be extended to odd numbers)

Lock-Freedom

- Lock-free: in an infinite execution infinitely often some method call finishes (obviously, in a finite number of steps)
- Pragmatic approach
- Implies no mutual exclusion



Lock-Free vs. Wait-free

• Wait-Free: each method call takes a finite number of steps to finish

• Lock-free: in an infinite execution infinitely often some method call finishes



Lock-Freedom



- Any wait-free implementation is lock-free.
- Lock-free is the same as waitfree if the execution is finite.
- Old saying: "Lock-free is to wait-free as deadlock-free is to lockout-free."

Lock-Free Implementations

- Lock-free consensus is just as impossible
- Lock-free = Wait-free for finite executions
- All the results we presented hold for lock-free algorithms also.

There is More: Universality

- Consensus is universal
- From n-thread consensus we can build
 - Wait-free/Lock-free,
 - Linearizable,
 - n-threaded,
 - Implementation
 - Of any sequentially specified object © Herlihy and Shavit 200